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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Louis et al.

SERIAL NO.: 09/837,850

PATENT NO.: 6,744,316 B2

FILED: April 19, 2001

ISSUED: June 1, 2004

DOCKET NO.: CE08635R

ENTITLED: A METHOD AND APPARATUS FOR REDUCTION OF DISTORTION IN
A TRANSMITTER



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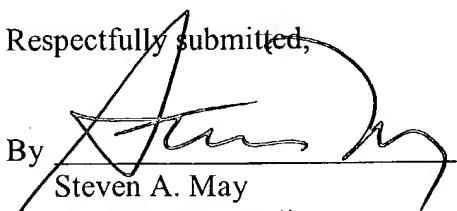
of Correction

In accordance with the provisions of 37 CFR § 1.322 of the Rules of Practice, which implement 35 USC § 254, the Patent and Trademark Office is respectfully requested to issue a Certificate of Correction in the above-identified patent. It is certified that errors appear in the above-identified patent as shown in the attached Certificate of Correction. Applicant certifies that the errors are of a minor character and were not the fault of Applicant. Since the changes necessary to correct these errors in the patent would not constitute new matter, and would not require re-examination, Applicant prays a Certificate of Correction will issue. Since errors were not the fault of Applicant, it is believed that there will not be a fee for this Certificate of Correction.

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Respectfully submitted,

By


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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.: 6,744,316

DATE: June 1, 2004

INVENTOR(S): Louis et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

The title should read --A METHOD AND APPARATUS FOR REDUCTION OF DISTORTION IN A
TRANSMITTER--

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PATENT NO. 6,744,316 B2

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(12) **United States Patent**
Louis et al.

(10) **Patent No.:** US 6,744,316 B2
(45) **Date of Patent:** Jun. 1, 2004

FOR
**(54) -METHOD AND APPARATUS FOR
REDUCTION OF DISTORTION IN A
TRANSMITTER**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 24 days.

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(51) Int. Cl.⁷ H03F 1/26

(52) U.S. Cl. 330/151; 330/149

(58) Field of Search 330/149, 151, 375/297

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(57) **ABSTRACT**

A transmitter includes a feed forward amplifier that includes main signal path having a radio frequency (RF) power amplifier, a feed forward correction circuit, and a control circuit. An amplification of an input signal by the RF power amplifier produces an amplified signal that includes a distortion component. The feed forward correction circuit produces an error signal, which error signal is used to drive an error amplifier to produce an amplified error signal. In order to minimize a distortion component introduced to the amplified error signal by the error amplifier, the control circuit controls a peak power of the error signal based on a detected energy of an attenuated version of the error signal or based on the distortion component introduced to the amplified error signal by the error amplifier.

12 Claims, 5 Drawing Sheets

